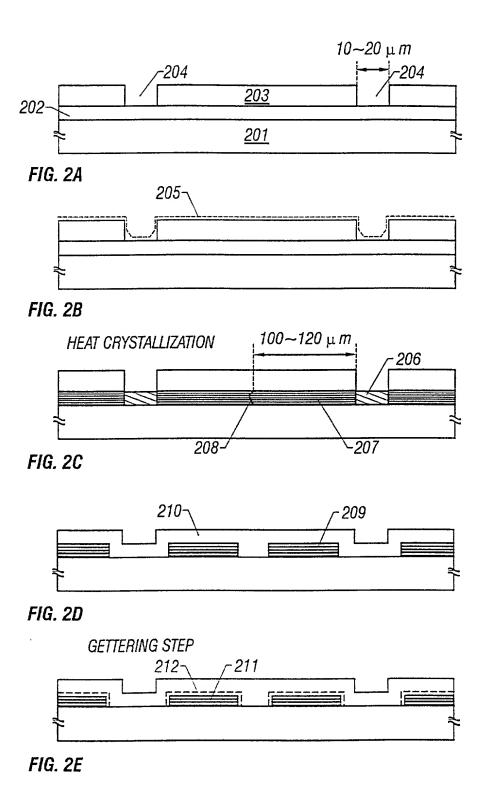


FIG. 1



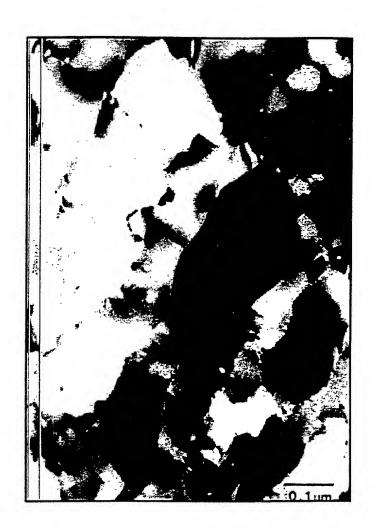
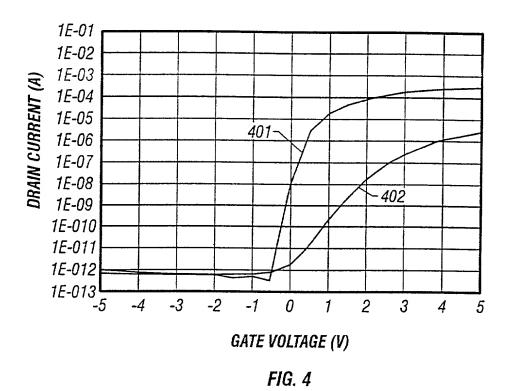
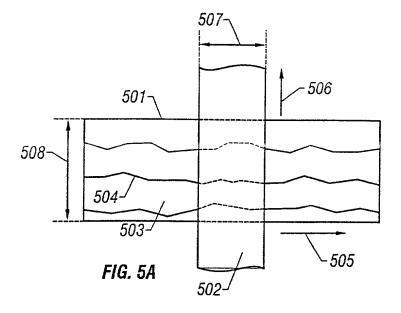


FIG. 3



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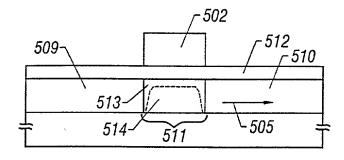


FIG. 5B

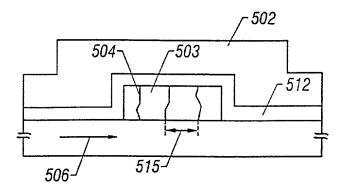


FIG. 5C

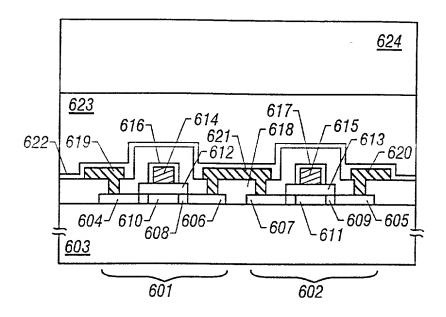


FIG. 6A

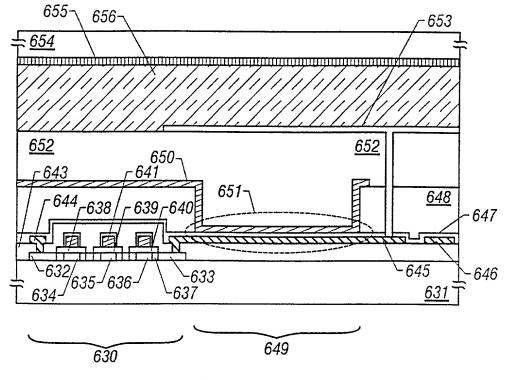


FIG. 6B

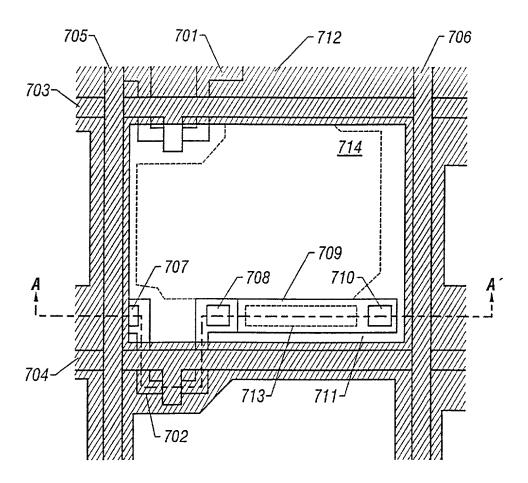
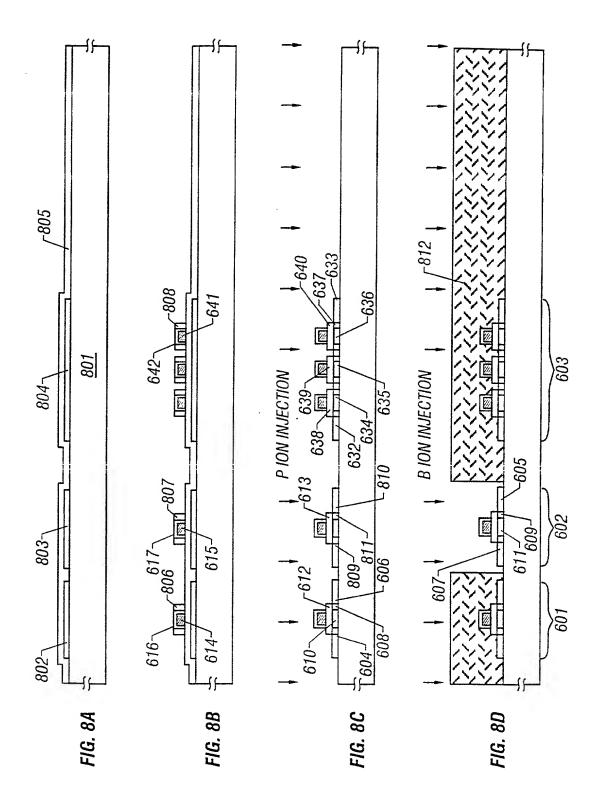


FIG. 7



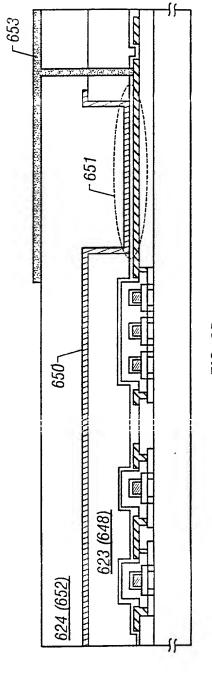
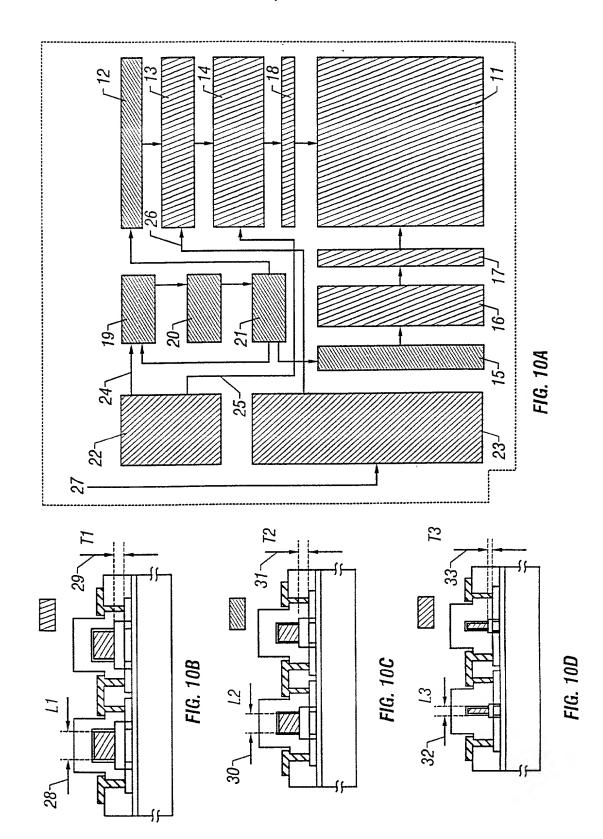
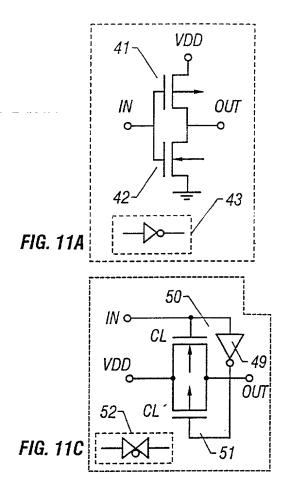


FIG. 9B

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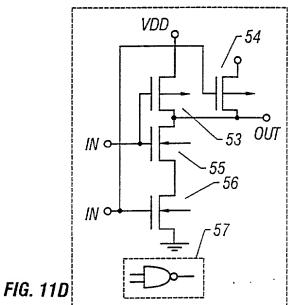
VDD 44

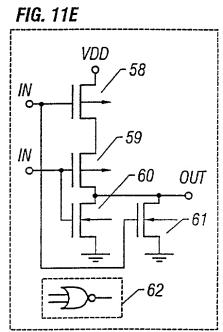
CL 45

IN OUT

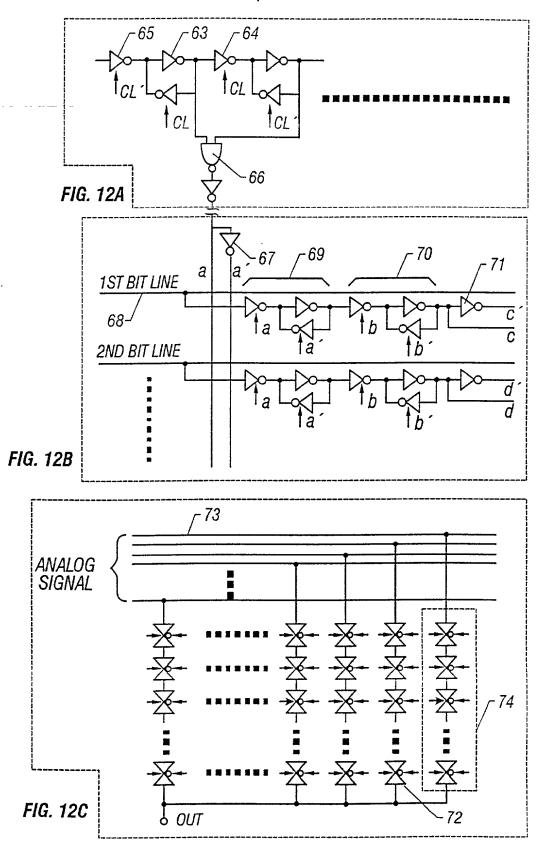
CL 47

47





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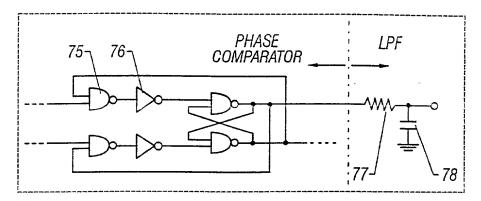


FIG. 13A

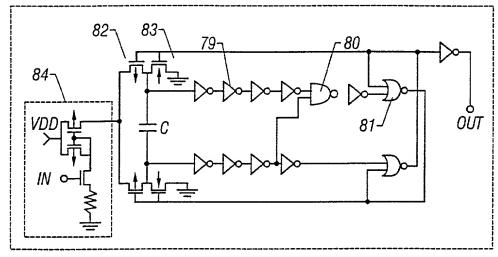


FIG. 13B

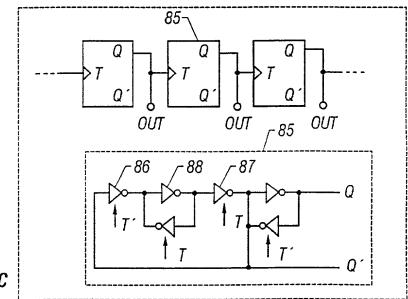


FIG. 13C

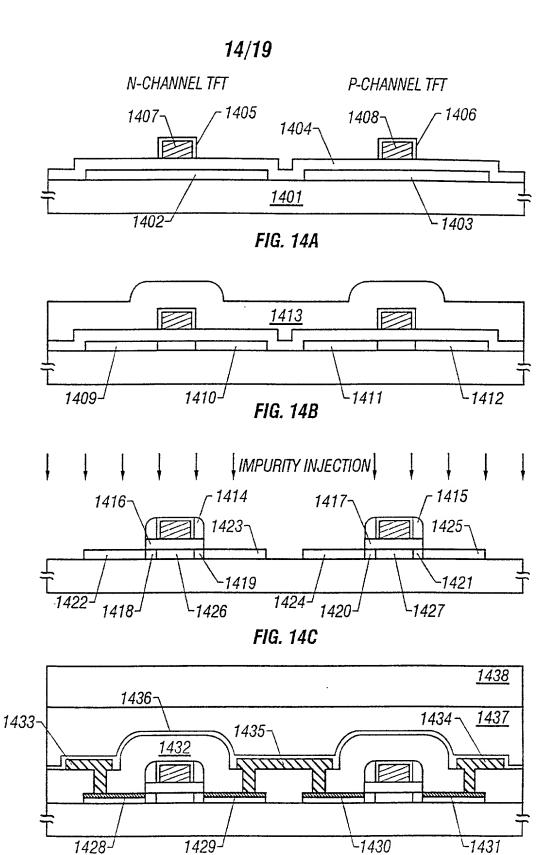


FIG. 14D

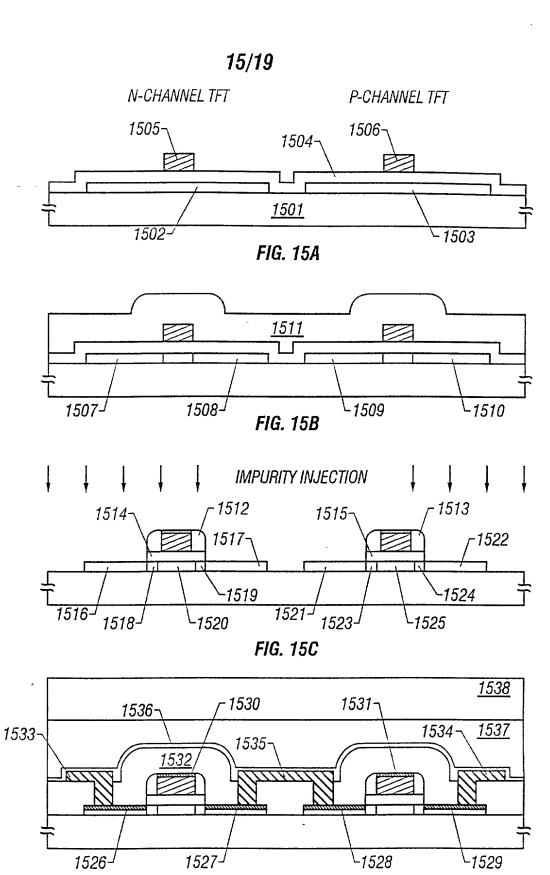


FIG. 15D

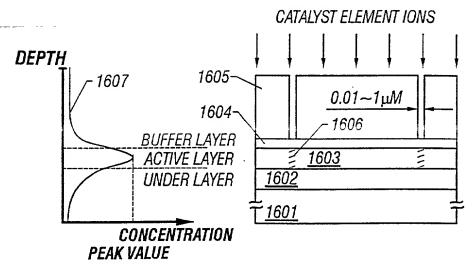


FIG. 16A

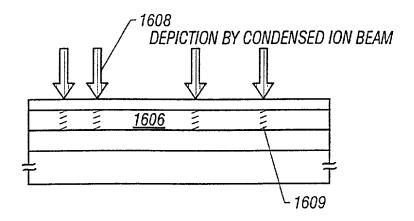


FIG. 16B

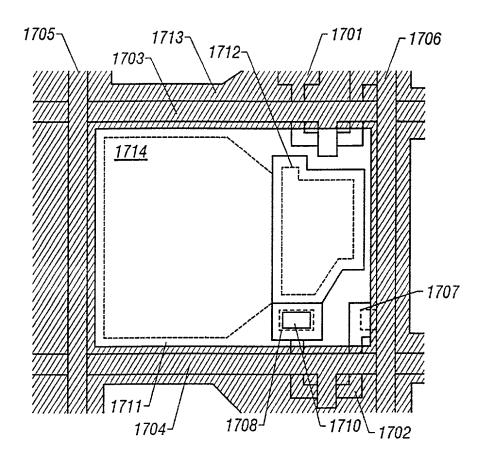
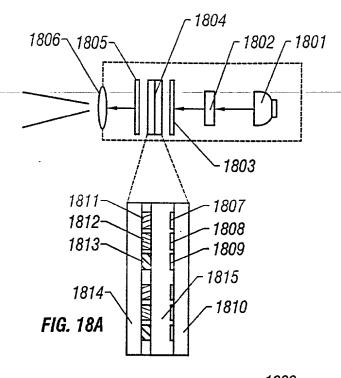


FIG. 17



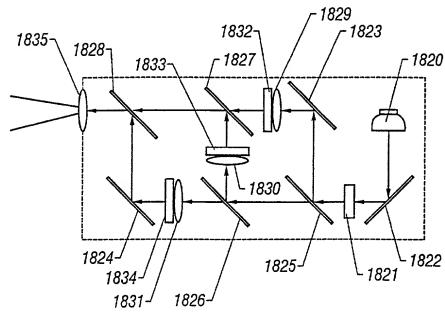


FIG. 18B

